

05/25/04

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MAY 24 2004

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22120 7590 03/03/2004

ZAGORIN O'BRIEN & GRAHAM, L.L.P.
 7600B N. CAPITAL OF TEXAS HWY.
 SUITE 350
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	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/990,894	11/16/2001	Roy E. Schuerlein	023-0013	3451

TITLE OF INVENTION: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD THEREFOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	06/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
YOHA, CONNIE C	2818	365-189070

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

Zagorin, O'Brien & Graham, LLP
 1
 2
 3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Matrix Semiconductor, Inc. Santa Clara, CA

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

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- ☒ Advance Order - # of Copies 10

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(Authorized Signature) (Date)

Andrew C. Graham, Reg. No. 36,531

5/23/04

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05/27/2004 WASFW2 00000053 09990894

01 FC:1501
 02 FC:8001

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